

ABSTRACT OF THE DISCLOSURE

An operation device having the element number and delay time of the operation circuitry reduced is realized by a purely logical approach. An operation method based on encoding is concretely and efficiently logic-designed to provide an encoding operation device. operators of an operation system are extended if required to treat as logic functions with the base number r . When $r=2$, using a generating function as a new representation of a mapping, a new operation system is logic-designed under encoding conditions and logic expression simplifying conditions, or the new operation system is logic-designed by matching the topologies of input/output relation of the operators of the original operation system and the new operation system. The operation device satisfying the encoding conditions and logic expression simplifying conditions achieves speeding up and low power consumption.